

transparent metal materials having a sheet resistance that is the same as a sheet resistance of the gate lines and data lines.

13. The array substrate of claim **12**, wherein patterns of the first polar plate of the first capacitor and the second polar plate of the first capacitor are different from a pattern of the first signal bus, and projections of the first polar plate of the first capacitor and the second polar plate of the first capacitor are partially overlapped with a projection of the first signal bus in a direction perpendicular to the substrate;

patterns of the first polar plate of the second capacitor and the second polar plate of the second capacitor are different from a pattern of the second signal bus, and projections of the first polar plate of the second capacitor and the second polar plate of the second capacitor are partially overlapped with a projection of the second signal bus in a direction perpendicular to the substrate.

14. A touch display panel, comprising the array substrate of claim **1**.

15. A touch display device, comprising the touch display panel of claim **14**.

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